

### Abstract of the Disclosure

Disclosed herein is an XY-addressing type solid-state imaging apparatus including: a plurality of pixels arranged in a two-dimensional matrix; and a horizontal and vertical scanning circuits for reading signals of the pixels, wherein the vertical scanning circuit concurrently selects the pixels of  $n$  rows ( $n$  being an integer of 2 or more) at a first timing to concurrently effect a reset operation of the pixels of the  $n$  rows thereof and selects at a second timing subsequent to the first timing the pixels of  $n$  rows of the address different from the rows selected at the first timing to effect a reset operation of the pixels of the  $n$  rows thereof, reset operation in this manner being repeated to effect a reset operation of all pixels.